AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing Of Claims:

1. (Currently Amended) A method for storing a computer program in a program memory of a control unit, the method comprising:

storing the computer program according to predefinable rules in specific memory areas of the program memory; and

storing predefinable information, which is used to transfer the control unit into a defined state, in unused memory areas of the program memory where the computer program is not stored, wherein the predefinable information is stored at predefinable intervals in at least one unused memory area of the program memory, a part of the unused memory area in which the predefinable information is not stored not causing at least one of a jump and an endless loop.

2. (Original) The method of claim 1, further comprising:

resetting the control unit by executing the predefinable information on a computing unit of the control unit.

3. (Original) The method of claim 1, wherein an interrupt service routine is invoked by executing the predefinable information on a computing unit of the control unit.

4. (Original) The method of claim 1, wherein an error handling routine is invoked by executing the predefinable information on a computing unit of the control unit.

5. (Original) The method of claim 3, wherein the control unit is reset at the end of the interrupt service routine.

6. (Original) The method of claim 1, wherein the predefinable information is stored in all unused memory areas of the program memory.

7. (Original) The method of claim 1, wherein at least one unused memory area of the program memory is completely filled using the predefinable information.

8. (Canceled)

9. (Currently Amended) The method of claim 1 8, wherein the predefinable information is stored at regular intervals in the at least one unused memory area of the program memory.

10. (Original) The method of claim 1, wherein the predefinable information is stored at an end of at least one unused memory area of the program memory, a part of the unused memory area in which the predefinable information is not stored not causing at least one of a jump and an endless loop.

11. (Currently Amended) A device for storing a computer program in a program memory of a control unit, the device comprising:

a first storing arrangement to store the computer program in specific memory areas of the program memory according to predefinable rules; and

a second storing arrangement to store predefinable information, which transfers the control unit into a defined state, in unused memory areas of the program memory, in which the first storing arrangement has not stored the computer program, wherein the predefinable information is stored at predefinable intervals in at least one unused memory area of the program memory, a part of the unused memory area in which the predefinable information is not stored not causing at least one of a jump and an endless loop.

12. (Original) The device of claim 11, wherein the second storing arrangement includes a hexadecimal editor.

13. (Original) The device of claim 11, wherein the device includes a resetting arrangement to reset the control unit by executing the predefinable information on a computing unit of the control unit.

14. (Original) The device of claim 11, wherein an interrupt service routine is invoked by executing the predefinable information on a computing unit of the control unit.

15. (Original) The device of claim 11, wherein an error handling routine is invoked by executing the predefinable information on a computing unit of the control unit.

16. (Original) The device of claim 14, wherein the control unit is reset at the end of the interrupt service routine.

17. (Original) The device of claim 11, wherein the predefinable information is stored in all unused memory areas of the program memory.

18. (Original) The device of claim 11, wherein at least one unused memory area of the program memory is completely filled using the predefinable information.

19. (Canceled)

20. (Currently Amended) The device of claim 11 19, wherein the predefinable information is stored at regular intervals in the at least one unused memory area of the program memory.

21. (Original) The device of claim 11, wherein the predefinable information is stored at an end of at least one unused memory area of the program memory, a part of the unused memory area in which the predefinable information is not stored not causing at least one of a jump and an endless loop.

22. (Original) The device of claim 15, wherein the control unit is reset at the end of the error handling routine.

23. (Currently Amended) A control unit comprising:
    a computing unit; and
    a program memory, at which a computer program is stored in specific memory areas of the program memory according to predefinable rules, wherein, in unused memory areas of the program memory, where the computer program is not stored, predefinable information is stored, which is used to transfer the control unit into a defined state, wherein the predefinable information is stored at predefinable intervals in at least one unused memory area of the program memory, a part of the unused memory area in which the predefinable information is not stored not causing at least one of a jump and an endless loop.

24. (Original) The control unit of claim 23, wherein the control unit includes an arrangement for resetting the control unit by executing the predefinable information on the computing unit of the control unit.

25. (Original) The control unit of claim 23, wherein an interrupt service routine is invoked by executing the predefinable information on the computing unit of the control unit.

26. (Original) The control unit of claim 23, wherein an error handling routine is invoked by executing the predefinable information on the computing unit of the control unit.

27. (Original) The control unit of claim 25, wherein the control unit is reset at the end of the interrupt service routine.

28. (Original) The control unit of claim 23, wherein the predefinable information is stored in all unused memory areas of the program memory.

29. (Original) The control unit of claim 23, wherein at least one unused memory area of the program memory is completely filled using the predefinable information.

30. (Canceled)

31. (Original) The control unit of claim 23, wherein the predefinable information is stored at regular intervals in the at least one unused memory area of the program memory.

32. (Original) The control unit of claim 23, wherein the predefinable information is stored at an end of at least one unused memory area of the program memory, a part of the unused memory area in which the predefinable information is not stored not causing at least one of a jump and an endless loop.

33. (Original) The control unit of claim 26, wherein the control unit is reset at the end of the error handling routine.

34. (Original) The method of claim 1, further comprising:
    resetting the control unit by executing the predefinable information on a microprocessor of the control unit.

35. (Original) The method of claim 4, wherein the control unit is reset at the end of the error handling routine.